

Teacher: **Mohsen Ramezani**

E-mail: m.ramezani@uok.ac.ir

Webpage: eng.uok.ac.ir/mramezani/teaching

References: 1-Computer Architecture (**Mano**)

Projects and Homeworks will be uploaded on the BBB (vclass.uok.ac.ir) every Thursday.

Evaluation: 45% Midtern+30% final exam+25% Homework.

Session No.	Week	Topic
Session 1	1	Introduction to computer systems
Session 2	1	Basic of computer systems
Session 3	2	Data representation in computers
Session 4	2	Data representation in computers
Session 5	3	Introduction to logical circuits
Session 6	3	Logical circuits-sequential circuits
Session 7	4	Digital devices
Session 8	4	Designing digital circuits
Session 9	5	Designing digital circuits-example
Session 10	5	Designing digital circuits-example
Session 11	6	Exam!
Session 12	6	Introduction to register transfer language
Session 13	7	Introduction to instructions
Session 14	7	Logical and arithmetic instructions
Session 15	8	Organization of a computer system
Session 16	8	Organization of a computer system-continue
Session 17	9	Basic computer
Session 18	9	Exam!
Session 19	10	Introduction to control
Session 20	10	Introduction to control-continue
Session 21	11	Structure of instructions
Session 22	11	Transferring and modifying data
Session 23	12	Exam!
Session 24	12	Parallel processing
Session 25	13	Parallel processing-continue
Session 26	13	RISC!
Session 27	14	Array Processor
Session 28	14	Exam!
Session 29	15	Input output organization
Session 30	15	Input output organization
Session 31	16	Presentations
Session 32	16	Overview